

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/005,766 Confirmation No.: 7390
Applicant(s): Antti Ruha et al.
Filed: 11/02/2001
Art Unit: 2618
Examiner: Tran, Pablo N.
Title: Multi-Mode I/O Circuitry Supporting Low Interference
 Signaling Schemes For High Speed Digital Interfaces
Attorney Docket No.: 872.0100.U1(US)
Customer No.: 29,683

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Information Disclosure Statement

(37 C.F.R. §1.97(b))

Sir:

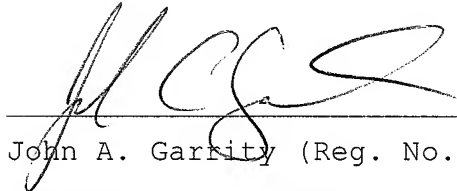
The following information is being disclosed to the U.S. Patent and Trademark Office as information that may be material to the examination of the above-identified patent application.

Applicant's Attorney is aware of the document listed on the enclosed Form PTO-1449.

The filing of this Statement is not to be construed as a representation that a search has been made regarding the claimed invention (37 C.F.R. §1.97(g)) or that no other possible material information exists. In addition, the filing of this Statement is not to be construed to be an admission that the information cited in the Statement is, or

is considered to be, material to Patentability (37 C.F.R.
§1.97(h)).

Respectfully submitted,



John A. Garrity (Reg. No. 60,470)



Date

Customer No.: 29683

Harrington & Smith, PC
4 Research Drive
Shelton, CT 06484-6212
203-925-9400